

Server Memory Compatibility Test Certificate

Test System: **Supermicro X9DAI**

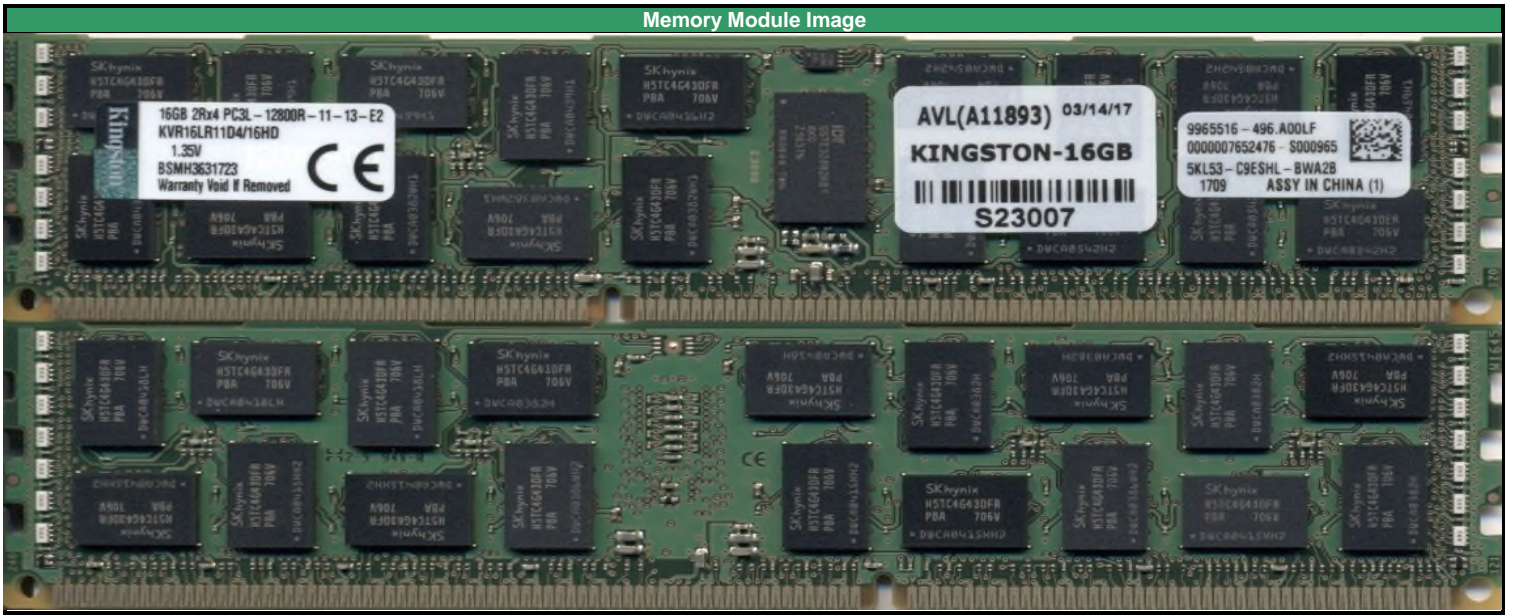
Test Result: **Pass**

Leveraged System(s): X9DR3-F, X9DRi-F, X9DRW-3F, X9DRW-iF, X9DA7, X9DR7-LN4F, X9DR7-JLN4F, X9DRE-LN4F, X9DRD-7LN4F, X9DRD-7JLN4F, X9DRD-EF, X9DRH-7F, X9DRH-7TF, X9DRH-iF, X9DRH-iTF, X9DRT-HF+, X9DRX+F, X9DRG-QF

| Module Specifications | | | | | | | | | | |
|-----------------------|------------------|----------------------------------|---------|------------------------|---------|------------------|----|-----|------|--------------|
| DIMM Vendor | DIMM Part Number | Type | Voltage | Size | Config. | Speed | CL | R/C | Rank | |
| Kingston | KVR16LR11D4/16HD | RDIMM | 1.35V | 16GB | 2Gx72 | 1600 | 11 | E | DR | |
| DRAM Vendor | DRAM Part Number | DRAM Density / Width / Date Code | | Register Vendor / Rev. | | DIMM Composition | | | | |
| Sk Hynix | H5TC4G43DFR-PBA | 4Gb | | 1024Mx4bit | | 1706 | | IDT | 6.4 | (2048Mx4)*72 |

| System Configuration | |
|----------------------|--|
| SETUP | System Information |
| AVL S/N | SN9376 |
| System S/N | UM23S30507 |
| Board Rev. (PBA) | 1234567890 |
| CPU Type | Intel E5-2650(IVB) x 2 |
| Chipset | Intel C602 |
| BIOS / Date | 3.2 /5/9/2015 |
| BMC / ME | n/a |
| FRU/SDR | n/a |
| OS | Red Hat Enterprise Linux 6.2 Kernel 2.6.32 |
| Test Tool | SAT, Memory Stress v1077 |

| Testing Summary | | |
|--|--|----------------------|
| Test Items | Test Description | Test Results |
| 1. Latest BIOS Upgrade & Configuration check | Record memory Size and Speed detection from BIOS | Done |
| 2. SPD Check | DIMM SPD content check for JEDEC compliance | Pass |
| 3. Reset Cycle | 1 DIMM Per Channel when applicable | Pass |
| 4a. Memory Stress | DIMM Loading per spec | 8 hours per config |
| 4b. Memory Benchmark | | 5 loops per config |
| 4c. Reset Cycle | | 200 loops per config |
| 5. Functional Stress Test | | 12 hours |
| Note: | | |



| AVL USE ONLY: | | | | | | |
|---------------|------------|------------------|-----------|--------|-------|---------|
| Completed by: | Van Nguyen | Completion Date: | 4/11/2017 | AVL A# | 11893 | AVL W/O |
| Comments: | | | | | | |