



**Advanced Validation Labs, Inc.**  
17665B Newhope Street, Fountain Valley, CA 92708 (714) 435-2630

### Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600GZ (Grizzly Pass)**

Test Result: **Pass**



Leveraged System(s):N/A

#### Module Information

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LR11S8/4I	RDIMM	1.35V	4GB	512Mx72	1600	11	A	SR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Hynix	HTC4G83BFR-PBA	4Gb	512Mx8bit	1452	IDT	A	(512Mx8)*72		

#### Leveraged Memory Modules

	Vendor	Type	Voltage	CL	Speed
1	Kingston	KVR16LR11S8K3/12I	RDIMM	1.35V	1600
2	Kingston	KVR16LR11S8K4/16I	RDIMM	1.35V	1600
3	Kingston	KVR16R11S8/4I	RDIMM	1.5V	1600
4	Kingston	KVR16R11S8K3/12I	RDIMM	1.5V	1600
5	Kingston	KVR16R11S8K4/16I	RDIMM	1.5V	1600
6					

#### System Configuration

SETUP	System #1	System #2
AVL S/N	SR2525	SO8251
System S/N	QSGR14500317	QSGR14600736
Board Rev. (PBA)	G11481-301	
CPU Type	E5-2697 v2 / 2.7 GHz	
Chipset	Intel C602	
BIOS / Date	02.03.2003 / 06/19/2014	
BMC / ME	1.19.5018 / 02.01.07.231	
FUR/SDR	1.13	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.5.3, SELViewer, Pvmode2, Syscfg, WinPIRA,MemPuller	

#### Testing Summary

Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	<b>Done</b>
2. SPD Check	DIMM SPD content check for JEDEC compliance	<b>Pass</b>
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot - A/E <b>Pass</b>
4. Memory Stress		HVDD Cold - B/F <b>Pass</b>
5. Memory Stress		LVDD Hot - C/G <b>Pass</b>
6. Memory Stress		LVDD Cold - D/H <b>Pass</b>
6. Power Cycle	Test each corner for 50 cycle in room temp	<b>Pass</b>
Note:		

#### Memory Module Image



#### AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	03/05/15	AVL A#	A10435	AVL W/O	WD3198
Comments:							

Test Results

4C					
Minimum Loading					
Start Date		2/28/2015			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV9081	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	SV9082	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	SV9083	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	SV9084	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	SV9085	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	SV9086	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	SV9087	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	SV9158	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Middle Loading					
Start Date		02/26/15			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV9081	P	P	P	P
CPU1 A2	SV9082	P	P	P	P
CPU1 A3					
CPU1 B1	SV9083	P	P	P	P
CPU1 B2	SV9084	P	P	P	P
CPU1 B3					
CPU1 C1	SV9085	P	P	P	P
CPU1 C2	SV9086	P	P	P	P
CPU1 C3					
CPU1 D1	SV9087	P	P	P	P
CPU1 D2	SV9088	P	P	P	P
CPU1 D3					
CPU2 E1	SV9089	P	P	P	P
CPU2 E2	SV9090	P	P	P	P
CPU2 E3					
CPU2 F1	SV9091	P	P	P	P
CPU2 F2	SV9092	P	P	P	P
CPU2 F3					
CPU2 G1	SV9093	P	P	P	P
CPU2 G2	SV9094	P	P	P	P
CPU2 G3					
CPU2 H1	SV9095	P	P	P	P
CPU2 H2	SV9157	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Maximum Loading					
Start Date		2/20/2015			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV9081	P	P	P	P
CPU1 A2	SV9082	P	P	P	P
CPU1 A3	SV9083	P	P	P	P
CPU1 B1	SV9084	P	P	P	P
CPU1 B2	SV9085	P	P	P	P
CPU1 B3	SV9086	P	P	P	P
CPU1 C1	SV9087	P	P	P	P
CPU1 C2	SV9088	P	P	P	P
CPU1 C3	SV9089	P	P	P	P
CPU1 D1	SV9090	P	P	P	P
CPU1 D2	SV9091	P	P	P	P
CPU1 D3	SV9092	P	P	P	P
CPU2 E1	SV9093	P	P	P	P
CPU2 E2	SV9094	P	P	P	P
CPU2 E3	SV9095	P	P	P	P
CPU2 F1	SV9096	P	P	P	P
CPU2 F2	SV9097	P	P	P	P
CPU2 F3	SV9098	P	P	P	P
CPU2 G1	SV9099	P	P	P	P
CPU2 G2	SV9100	P	P	P	P
CPU2 G3	SV9101	P	P	P	P
CPU2 H1	SV9102	P	P	P	P
CPU2 H2	SV9103	P	P	P	P
CPU2 H3	SV9161	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Minimum Loading					
Start Date		3/2/2015			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV9081	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	SV9082	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	SV9083	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	SV9084	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	SV9085	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	SV9086	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	SV9087	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	SV9088	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Middle Loading					
Start Date		02/25/15			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV9081	P	P	P	P
CPU1 A2	SV9082	P	P	P	P
CPU1 A3					
CPU1 B1	SV9083	P	P	P	P
CPU1 B2	SV9084	P	P	P	P
CPU1 B3					
CPU1 C1	SV9085	P	P	P	P
CPU1 C2	SV9086	P	P	P	P
CPU1 C3					
CPU1 D1	SV9087	P	P	P	P
CPU1 D2	SV9088	P	P	P	P
CPU1 D3					
CPU2 E1	SV9089	P	P	P	P
CPU2 E2	SV9090	P	P	P	P
CPU2 E3					
CPU2 F1	SV9091	P	P	P	P
CPU2 F2	SV9092	P	P	P	P
CPU2 F3					
CPU2 G1	SV9093	P	P	P	P
CPU2 G2	SV9094	P	P	P	P
CPU2 G3					
CPU2 H1	SV9095	P	P	P	P
CPU2 H2	SV9096	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Maximum Loading					
Start Date		2/16/2015			
DIMM Voltage		1.35v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV9081	P	P	P	P
CPU1 A2	SV9082	P	P	P	P
CPU1 A3	SV9083	P	P	P	P
CPU1 B1	SV9084	P	P	P	P
CPU1 B2	SV9085	P	P	P	P
CPU1 B3	SV9086	P	P	P	P
CPU1 C1	SV9087	P	P	P	P
CPU1 C2	SV9088	P	P	P	P
CPU1 C3	SV9089	P	P	P	P
CPU1 D1	SV9090	P	P	P	P
CPU1 D2	SV9091	P	P	P	P
CPU1 D3	SV9092	P	P	P	P
CPU2 E1	SV9093	P	P	P	P
CPU2 E2	SV9094	P	P	P	P
CPU2 E3	SV9095	P	P	P	P
CPU2 F1	SV9096	P	P	P	P
CPU2 F2	SV9097	P	P	P	P
CPU2 F3	SV9098	P	P	P	P
CPU2 G1	SV9099	P	P	P	P
CPU2 G2	SV9100	P	P	P	P
CPU2 G3	SV9101	P	P	P	P
CPU2 H1	SV9102	P	P	P	P
CPU2 H2	SV9103	P	P	P	P
CPU2 H3	SV9104	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P