

**Server Memory Compatibility Test Certificate**

Test System: **Supermicro H8DGi-F** Test Result: **Pass**

Leveraged System(s): H8DGI

**Module Specifications**

Rev. 1/13/2015

| DIMM Vendor | DIMM Part Number | Type                             | Voltage | Size | Config.                | Speed | CL               | R/C | Rank |
|-------------|------------------|----------------------------------|---------|------|------------------------|-------|------------------|-----|------|
| Kingston    | KVR16LE11-8HD    | UDIMM ECC                        | 1.35V   | 8GB  | 1Gx72                  | 1600  | 11               | E   | DR   |
| DRAM Vendor | DRAM Part Number | DRAM Density / Width / Date Code |         |      | Register Vendor / Rev. |       | DIMM Composition |     |      |
| Sk Hynix    | H5TC4G83DFR-PBA  | 4Gb 512Mx8bit 1704               |         |      | N/A                    |       | (1024Mx8)*72     |     |      |

**System Configuration**

| SETUP            | System Information                         |
|------------------|--|
| AVL S/N          | SL2664                                     |
| System S/N       | 2M0CS7017                                  |
| Board Rev. (PBA) | 1.00                                       |
| CPU Type         | AMD Opteron 6128 x2                        |
| Chipset          | AMD SR5690 CIMx 1.0.1.0                    |
| BIOS / Date      | 3.5c /3/18/16                              |
| BMC / ME         | n/a  |
| FRU/SDR          | n/a  |
| OS               | Red Hat Enterprise Linux 6.2 Kernel 2.6.32 |
| Test Tool        | SAT, Memory Stress v1077                   |

**Testing Summary**

| Test Items                                   | Test Description                                 | Test Results         |
|--|--|----------------------|
| 1. Latest BIOS Upgrade & Configuration check | Record memory Size and Speed detection from BIOS | Done                 |
| 2. SPD Check                                 | DIMM SPD content check for JEDEC compliance      | Pass                 |
| 3. Reset Cycle                               | 1 DIMM Per Channel when applicable               | Pass                 |
| 4a. Memory Stress                            | DIMM Loading per spec                            | 8 hours per config   |
| 4b. Memory Benchmark                         |  | 5 loops per config   |
| 4c. Reset Cycle                              |  | 200 loops per config |
| 5. Functional Stress Test                    |  | 12 hours             |
| Note:  |  |                      |

**Memory Module Image**



**AVL USE ONLY:**

|               |            |                  |           |        |       |         |        |
|---------------|------------|------------------|-----------|--------|-------|---------|--------|
| Completed by: | Van Nguyen | Completion Date: | 3/23/2017 | AVL A# | 11890 | AVL W/O | WD7717 |
| Comments:     |            |                  |           |        |       |         |        |